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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicate Fernando Gonzalez

Title: VERTICAL TRANSISTOR AND METHOD OF MAKING

Docket No.: 303.776US1

Filed: August 30, 2001

Examiner: Douglas W Owens

Customer No.: 21186

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

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Serial No.: 09/945,495

Due Date: August 24, 2004

Group Art Unit: 2811

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